## Communication Protocols

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#### Communication Protocols I

- Introduction
- UART
- SPI
- |2C

#### Introduction

- What do mean by Communication?
- Mode of Communications
- Type of Communications
- Why Protocols?

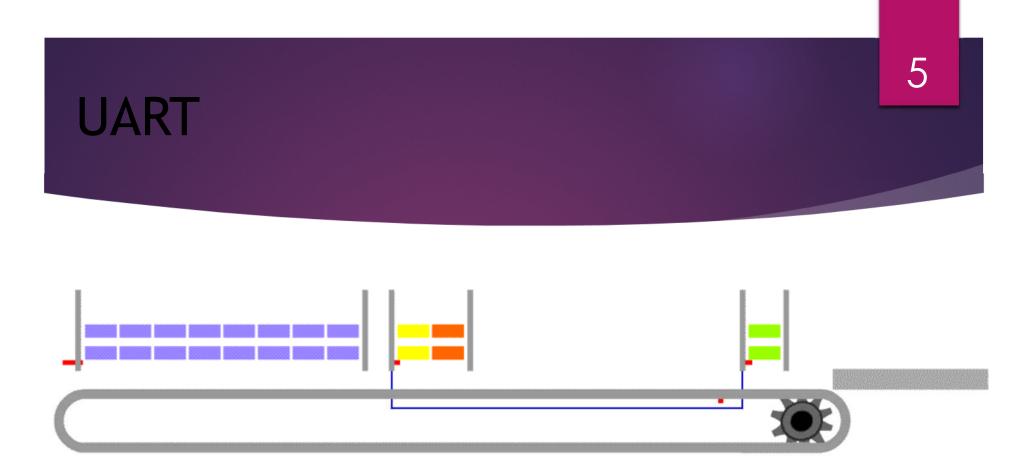
### Introduction

**Modes of Communication** 

Simplex

Half Duplex

Duplex



### Serial Peripheral Interface

- Introduction
- Interface
- Hardware Configurations
- Data Transmission
  - Data Validity

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### SPI Introduction

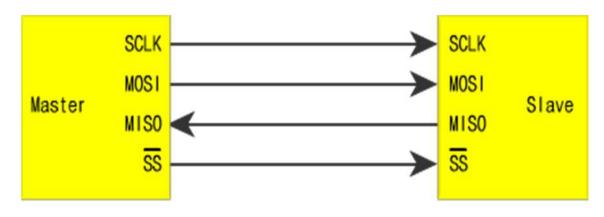
- Synchronous
- Full Duplex
- Master / Slave

### SPI Interface

- SCLK
- MOSI
- MISO
- nSS

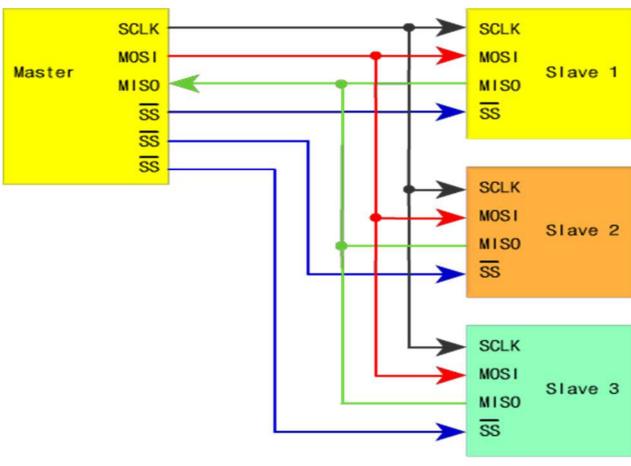
9

# **SPI**Hardware Configuration



Single Master and Single Slave

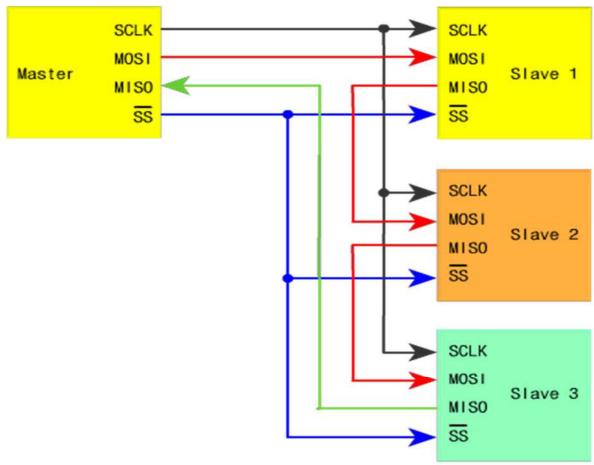
## SPI Hardware Configuration

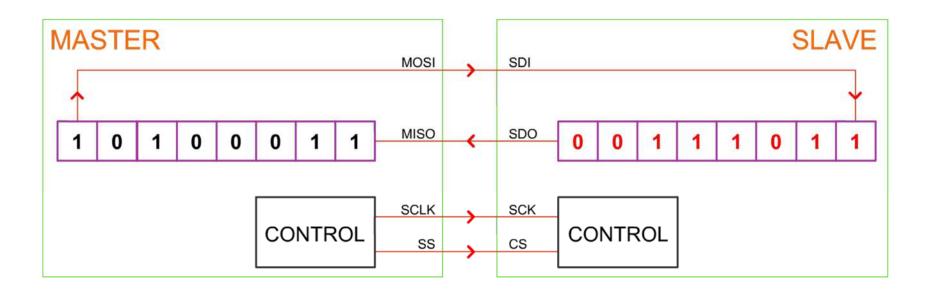


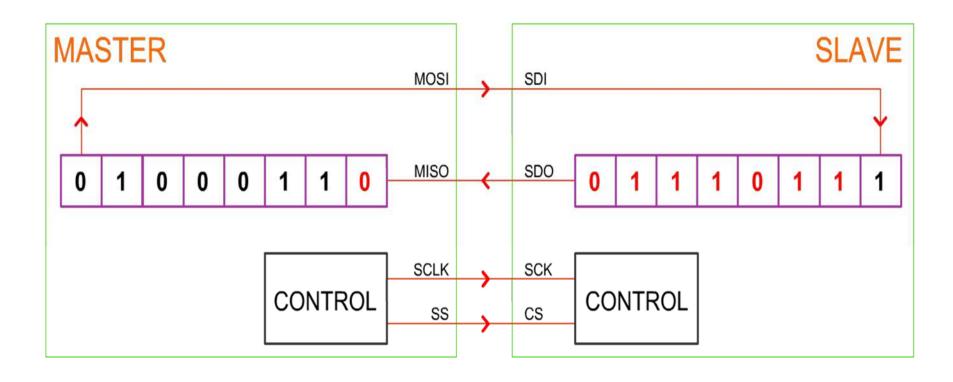
Single Master and three Slaves

SPI

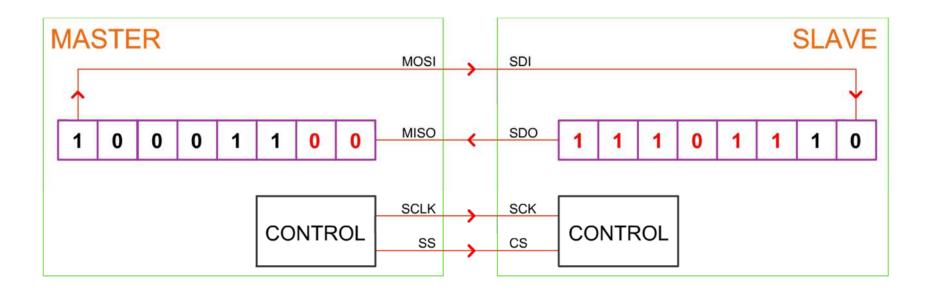
#### **Hardware Configuration**



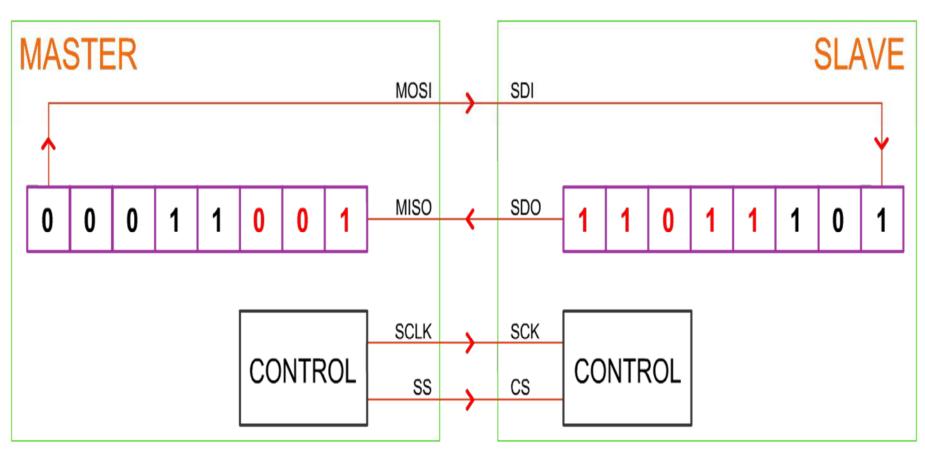


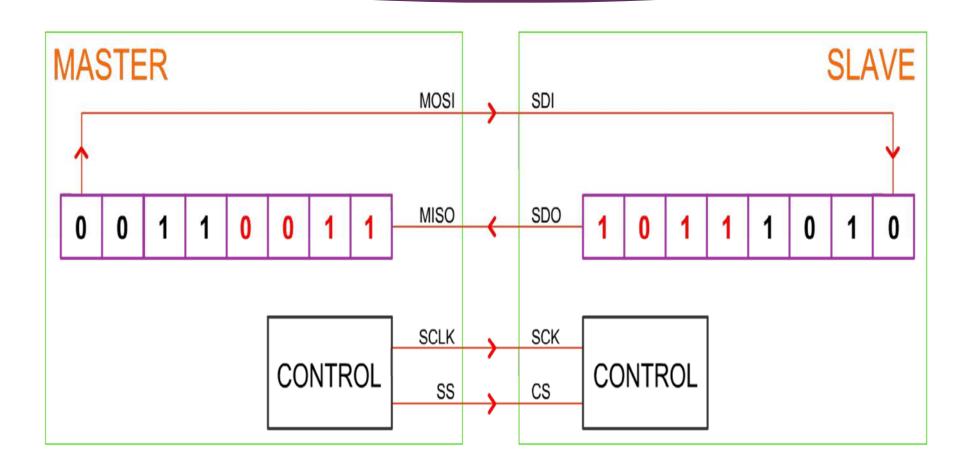


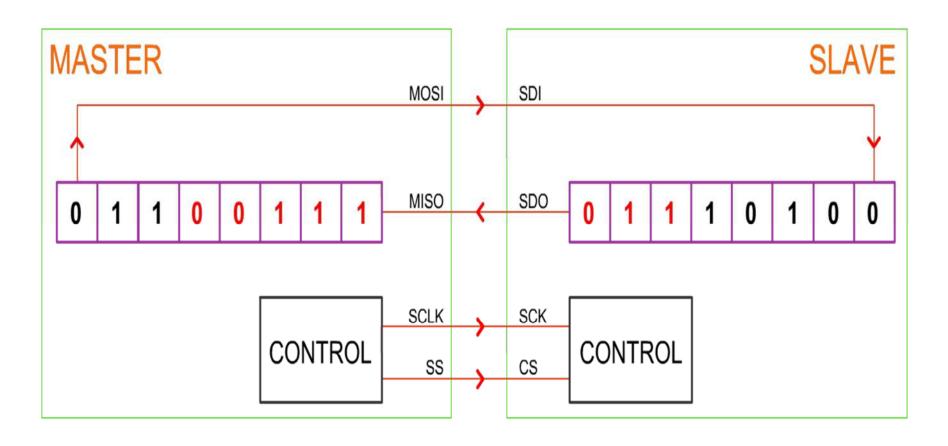


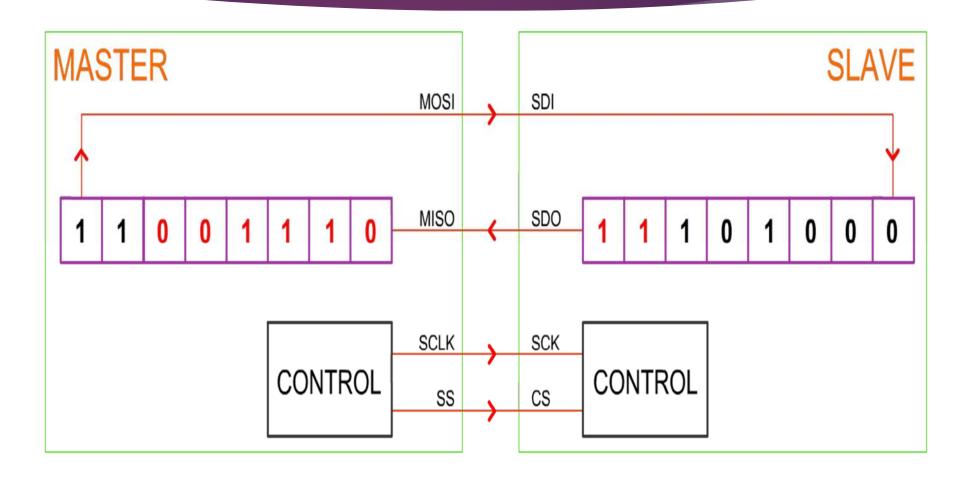


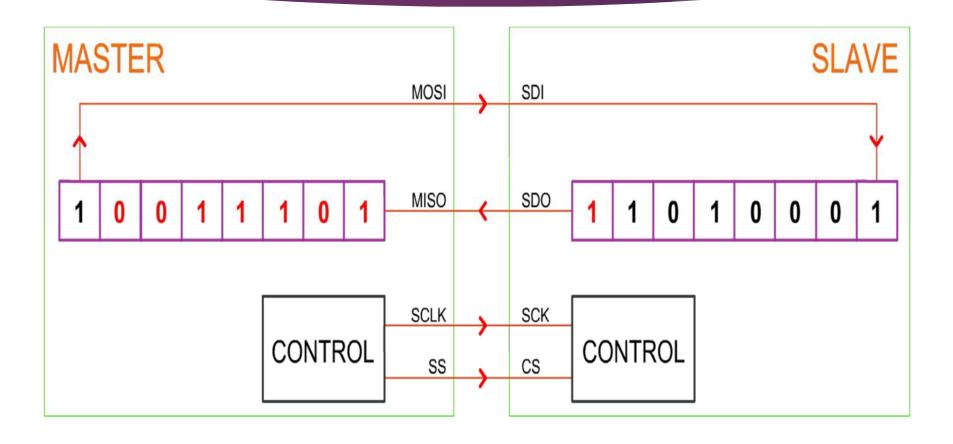


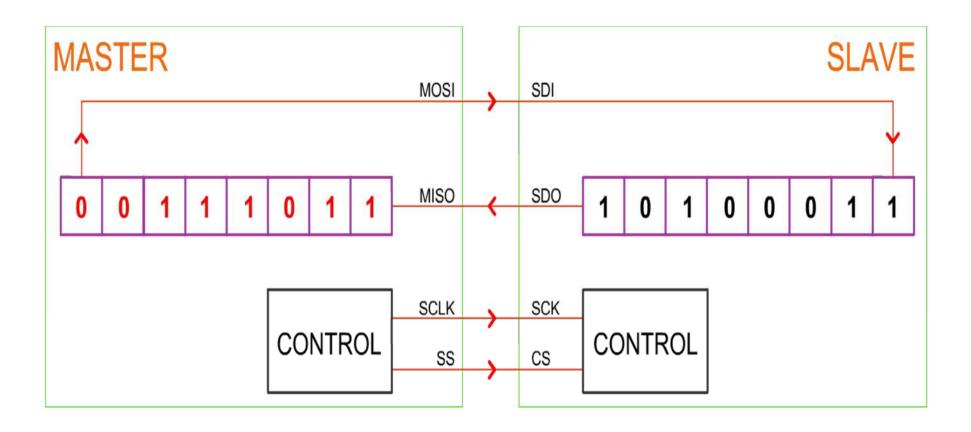




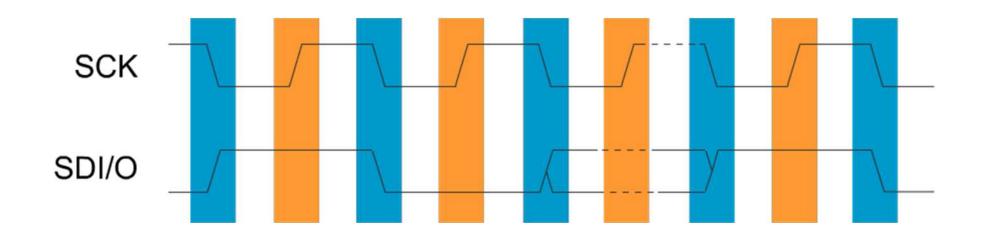








## SPI Data Validity



Data Change

Data Read

## **Inter Integrated Circuits**

- Introduction
- Bus Features
- The Protocol
- Bus Speeds

### I<sup>2</sup>C Introduction

- Synchronous
- Half Duplex
- Multi Master / Slave

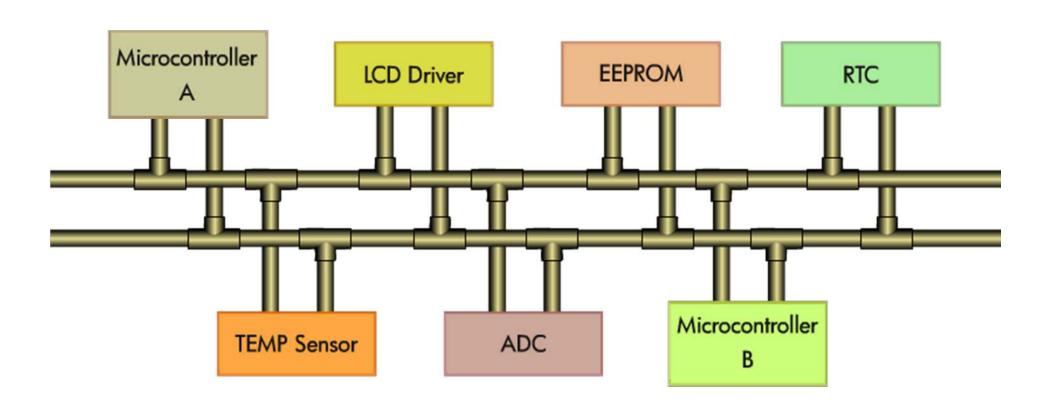
## I<sup>2</sup>C Bus Features

- Two Line Interface
- Software Addressable
- Multi Master with CD
- Serial, 8 bit Oriented, Bidirectional with 4 Modes
- On Chip Filtering

### I<sup>2</sup>C Protocol

- Example
- Signals
- A Complete Data Transfer

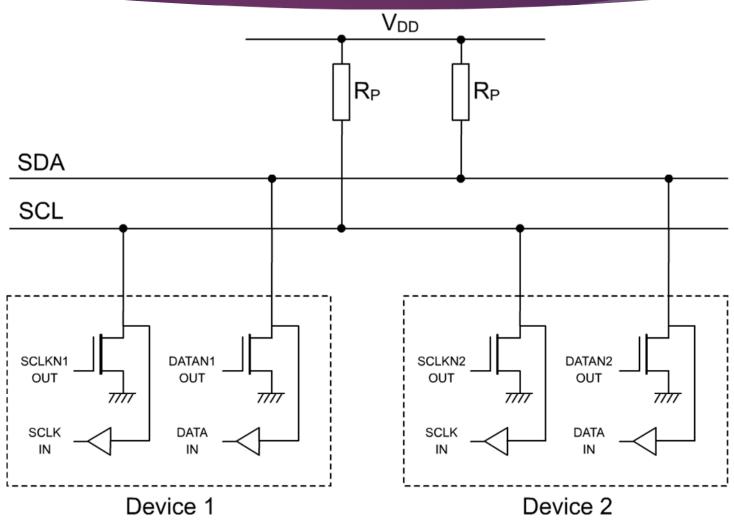
## I<sup>2</sup>C Example



### I<sup>2</sup>C Signals

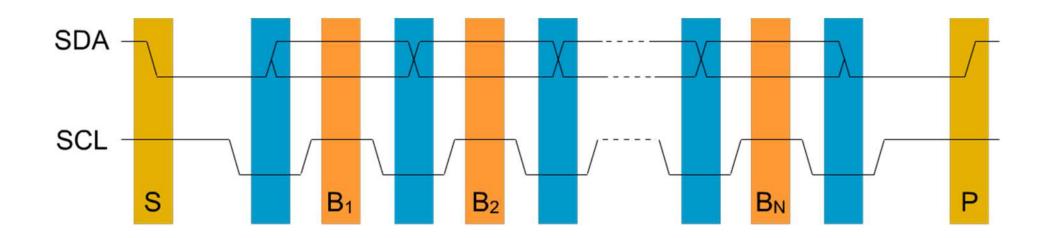
- Two-wired Interface
  - SDA
  - SCL
- Wired-AND
- Conditions and Data Validity
- Transmission

## I<sup>2</sup>C Signals - Wired-AND



I<sup>2</sup>C

#### Signals - Conditions and Data Validity



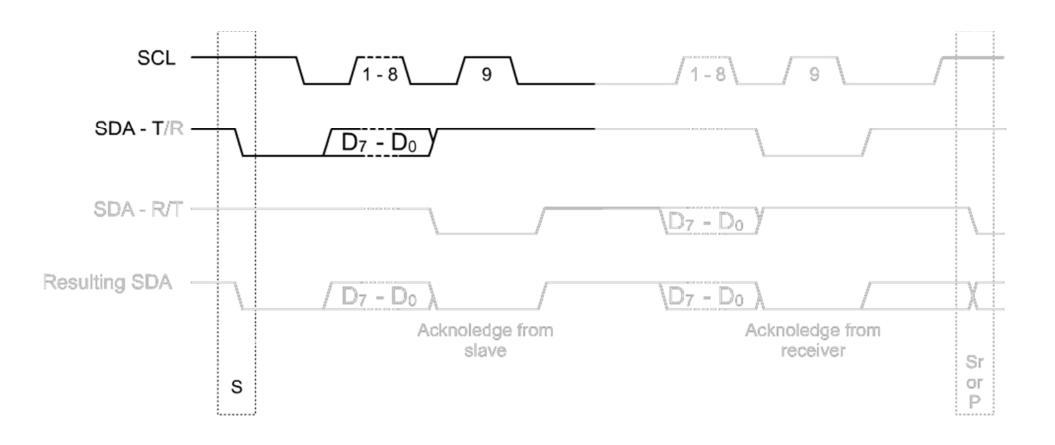
Conditions

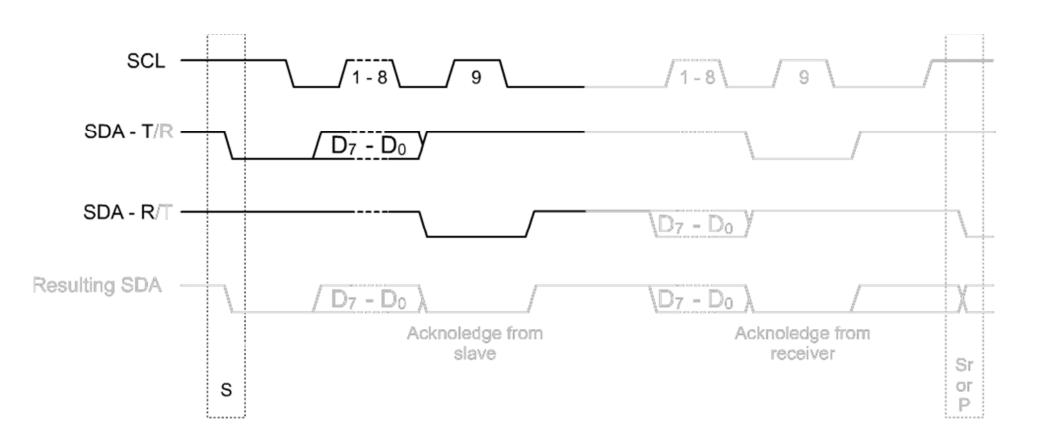
Data Change

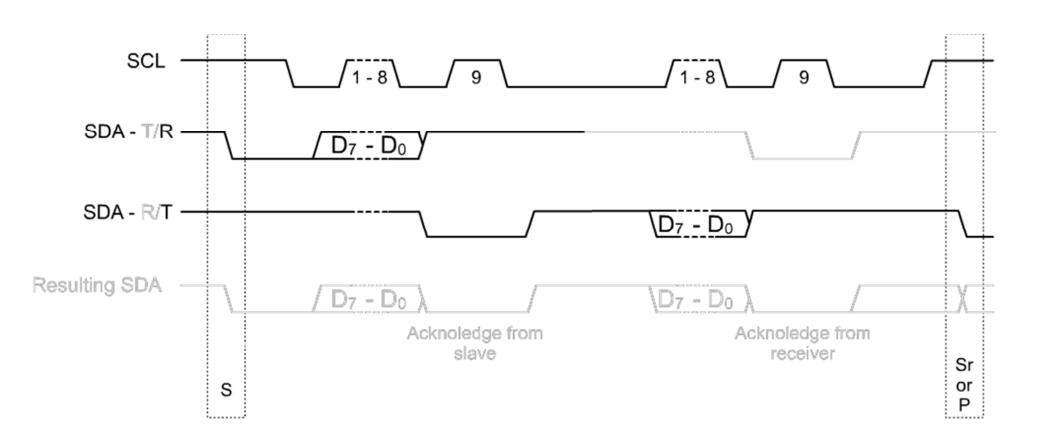
Valid Data

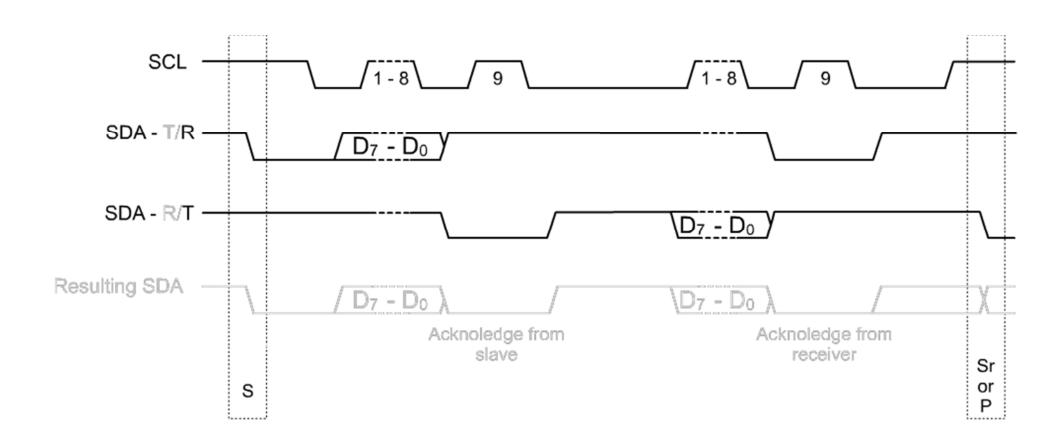
# I<sup>2</sup>C Signals - Transmission

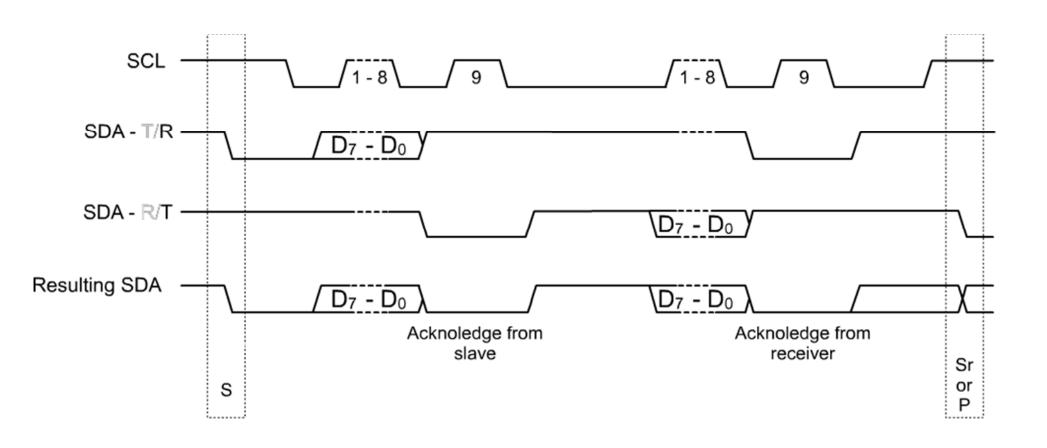
- Data on SDA
- Clocking on SCL
- Clock Synchronization
- Data Arbitration



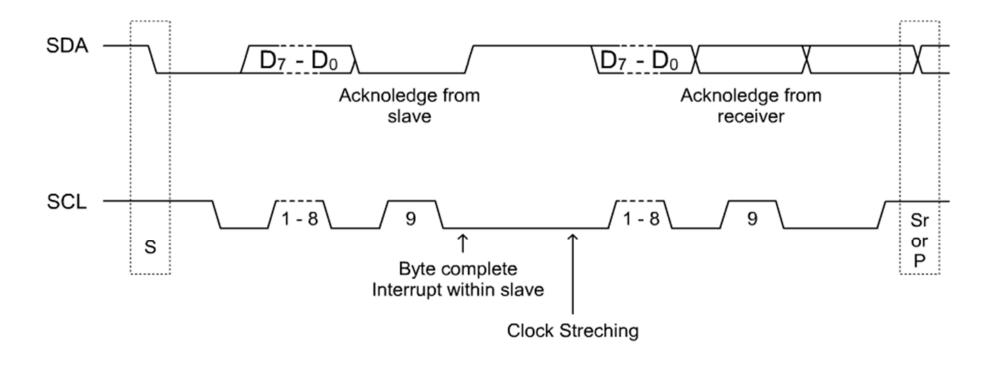




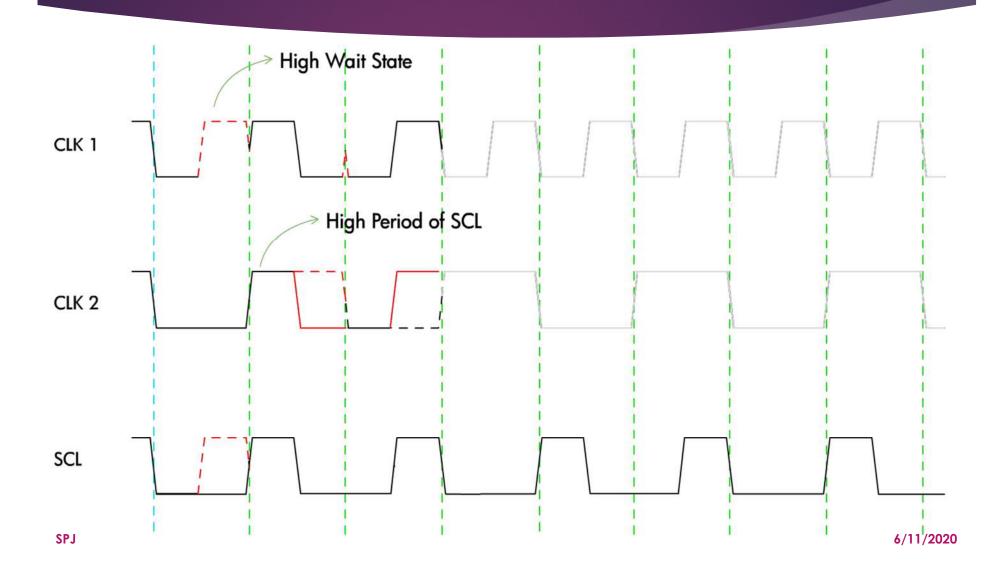




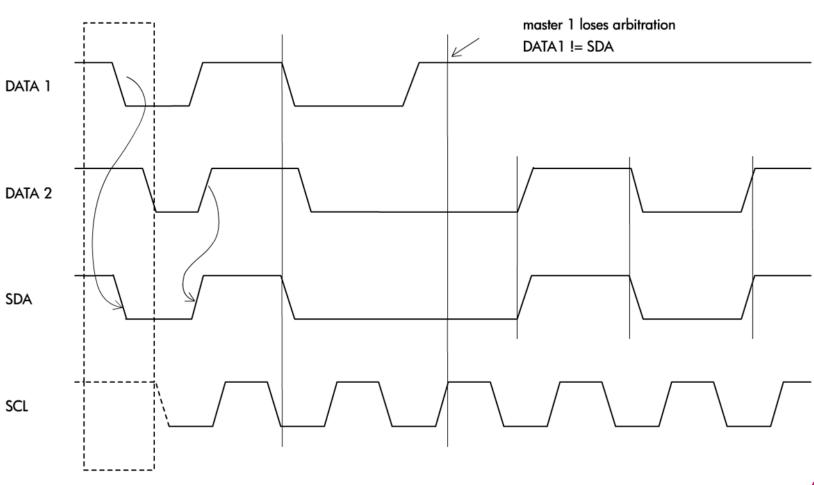
# I<sup>2</sup>C Signals - Clocking on SCL



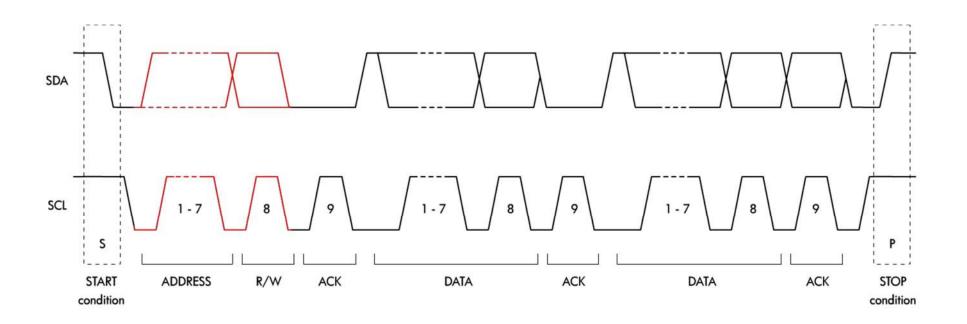
# I<sup>2</sup>C Signals - Clock Synchronization



# I<sup>2</sup>C Signals - Data Arbitration



# I<sup>2</sup>C A Complete Data Transfer



### I<sup>2</sup>C Bus Speeds

- Bidirectional Bus
  - Standard Mode 100 Kbit/s
  - Fast Mode 400 Kbits/s
  - Fast Mode Plus 1 Mbits/s
  - High Speed Mode 3.4 Mbits/s
- Unidirectional Bus
  - Ultra Fast Mode 5 Mbits/s
    - Uses Push-Pull Drivers (No Pullups)