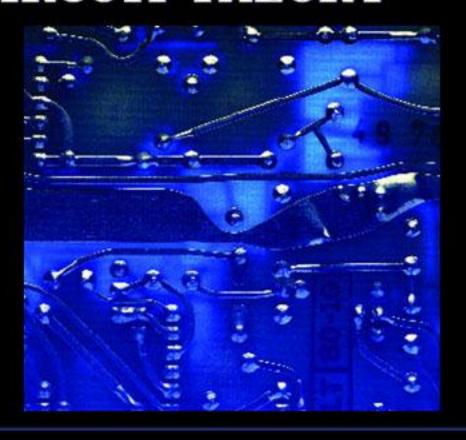
ELECTRONIC DEVICES AND CIRCUIT THEORY

TENTH EDITION

BOYLESTAD



PEARSON

Chapter 5: BJT AC Analysis

BJT Transistor Modeling

- A model is an equivalent circuit that represents the AC characteristics of the transistor.
- A model uses circuit elements that approximate the behavior of the transistor.
- There are two models commonly used in small signal AC analysis of a transistor:
 - r_e model
 - Hybrid equivalent model

The r_e Transistor Model

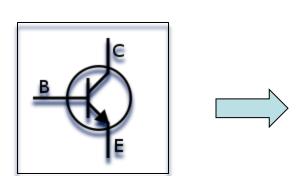
- BJTs are basically current-controlled devices; therefore the $r_{\rm e}$ model uses a diode and a current source to duplicate the behavior of the transistor.
- One disadvantage to this model is its sensitivity to the DC level. This model is designed for specific circuit conditions.

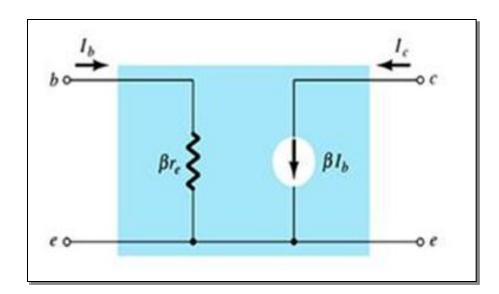


The r_e Transistor Model

Common-Emitter Configuration

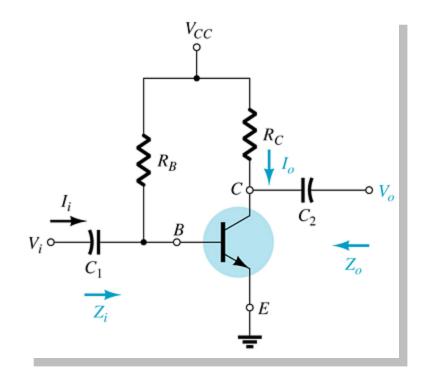
• The equivalent circuit of Fig above will be used throughout the analysis to follow for the common-emitter configuration.



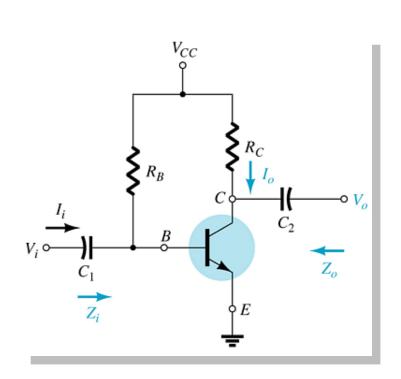


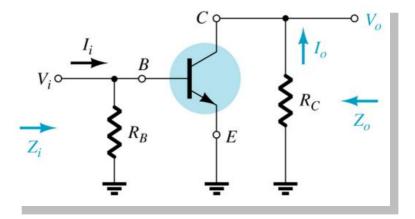
Common-Emitter Fixed-Bias Configuration

- The input is applied to the base
- The output is from the collector
- High input impedance
- Low output impedance
- High voltage and current gain
- Phase shift between input and output is 180°

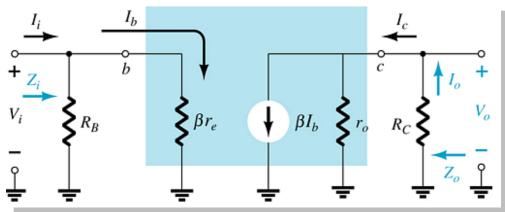


Common-Emitter Fixed-Bias Configuration





AC equivalent



r_e model



Common-Emitter Fixed-Bias Calculations

Input impedance:

$$Z_{i} = R_{B} || \beta r_{e}$$

$$Z_{i} \cong \beta r_{e} ||_{R_{E} \ge 10\beta r_{e}}$$

Output impedance:

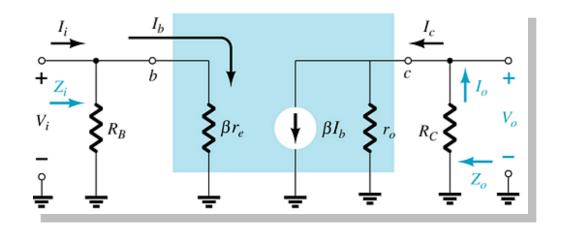
$$\mathbf{Z}_{o} = \mathbf{R}_{\mathbf{C}} || \mathbf{r}_{\mathbf{O}}$$

$$\mathbf{Z}_{o} \cong \mathbf{R}_{\mathbf{C}} ||_{\mathbf{r}_{o} \ge 10\mathbf{R}_{\mathbf{C}}}$$

Voltage gain:

$$\mathbf{A}_{\mathbf{v}} = \frac{\mathbf{V}_{\mathbf{o}}}{\mathbf{V}_{\mathbf{i}}} = -\frac{(\mathbf{R}_{\mathbf{C}} || \mathbf{r}_{\mathbf{o}})}{\mathbf{r}_{\mathbf{e}}}$$

$$A_{v} = -\frac{R_{C}}{r_{e}} \Big| r_{o} \ge 10R_{C}$$



Current gain:

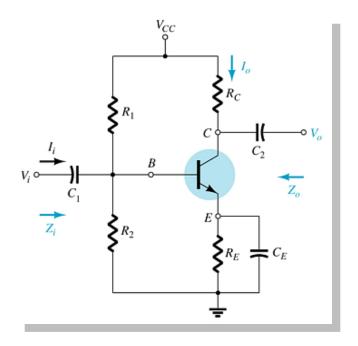
$$\begin{aligned} \mathbf{A_i} &= \frac{\mathbf{I_o}}{\mathbf{I_i}} = \frac{\beta \mathbf{R_B} \mathbf{r_o}}{(\mathbf{r_o} + \mathbf{R_C})(\mathbf{R_B} + \beta \mathbf{r_e})} \\ \mathbf{A_i} &\cong \beta \Big|_{\mathbf{r_o} \geq 10\mathbf{R_C}, \mathbf{R_B} \geq 10\beta \mathbf{r_e}} \end{aligned}$$

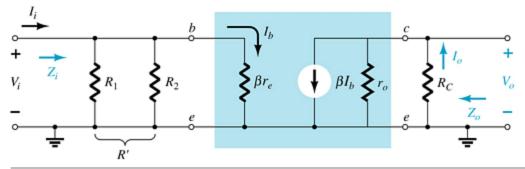
Current gain from voltage gain:

$$A_i = -A_v \frac{Z_i}{R_C}$$



Common-Emitter Voltage-Divider Bias





 r_e model requires you to determine β , r_e , and r_o .

Common-Emitter Voltage-Divider Bias Cal.

Input impedance:

$$R' = R_1 || R_2$$
$$Z_i = R' || \beta r_e$$

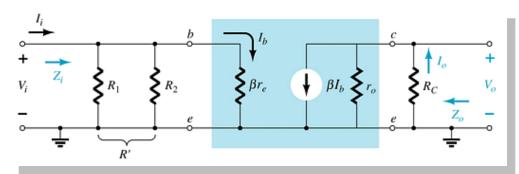
Output impedance:

$$Z_{0} = R_{C} || r_{0}$$

$$Z_{0} \cong R_{C} ||_{r_{0} \ge 10R_{C}}$$

Voltage gain:

$$A_{v} = \frac{V_{o}}{V_{i}} = \frac{-R_{C} || r_{o}}{r_{e}}$$
$$A_{v} = \frac{V_{o}}{V_{i}} \cong -\frac{R_{C}}{r_{e}} || r_{o} \ge 10R_{C}$$



Current gain:

$$A_{i} = \frac{I_{o}}{I_{i}} = \frac{\beta R' r_{o}}{(r_{o} + R_{C})(R' + \beta r_{e})}$$

$$A_{i} = \frac{I_{o}}{I_{i}} \cong \frac{\beta R'}{R' + \beta r_{e}} \Big|_{r_{o} \ge 10R_{C}}$$

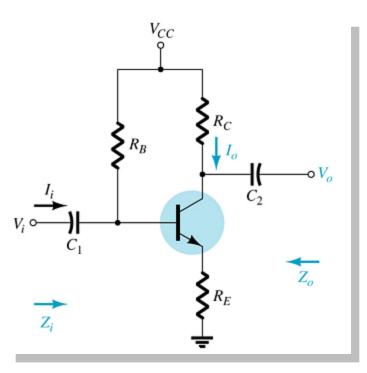
$$\mathbf{A_i} = \frac{\mathbf{I_o}}{\mathbf{I_i}} \cong \beta \Big|_{\mathbf{r_o} \ge 10\mathbf{R_C}, \mathbf{R'} \ge 10\beta\mathbf{r_e}}$$

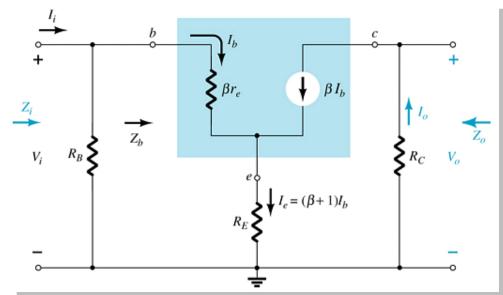
Current gain from voltage gain:

$$A_i = -A_v \frac{Z_i}{R_C}$$



Common-Emitter Emitter-Bias Config.





Impedance Calculations

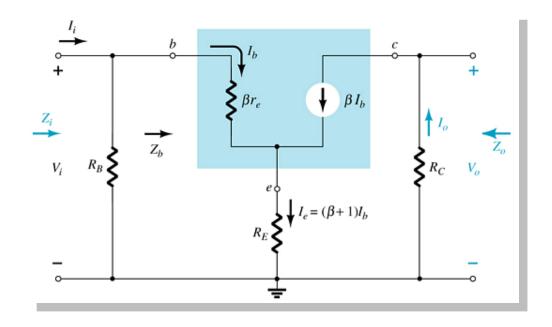
Input impedance:
$$Z_i = R_B || Z_b$$

$$Z_b = \beta r_e + (\beta + 1) R_E$$

$$Z_b \cong \beta (r_e + R_E)$$

$$Z_b \cong \beta R_E$$

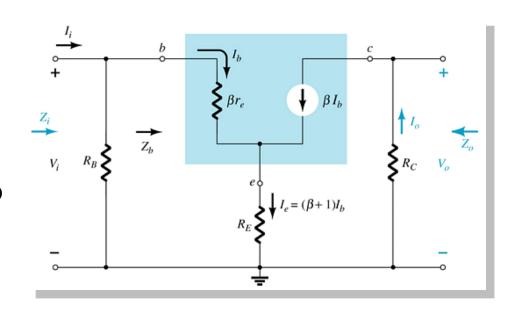
Output impedance: $Z_0 = R_C$



Gain Calculations

Voltage gain:

$$\begin{aligned} \mathbf{A}_{v} &= \frac{\mathbf{V}_{o}}{\mathbf{V}_{i}} = -\frac{\beta \mathbf{R}_{C}}{\mathbf{Z}_{b}} \\ \mathbf{A}_{v} &= \frac{\mathbf{V}_{o}}{\mathbf{V}_{i}} = -\frac{\mathbf{R}_{C}}{\mathbf{r}_{e} + \mathbf{R}_{E}} \Big|_{\mathbf{Z}_{b} = \beta(\mathbf{r}_{e} + \mathbf{R}_{E})} \\ \mathbf{A}_{v} &= \frac{\mathbf{V}_{o}}{\mathbf{V}_{i}} \cong -\frac{\mathbf{R}_{C}}{\mathbf{R}_{E}} \Big|_{\mathbf{Z}_{b} \cong \beta \mathbf{R}_{E}} \end{aligned}$$



Current gain:

$$A_i = \frac{I_o}{I_i} = \frac{\beta R_B}{R_B + Z_b}$$

Current gain from voltage gain:

$$\mathbf{A_i} = -\mathbf{A_v} \frac{\mathbf{Z_i}}{\mathbf{R_C}}$$

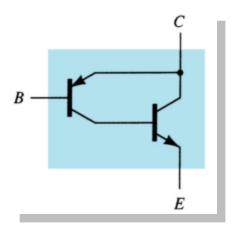
Feedback Pair

This is a two-transistor circuit that operates like a Darlington pair, but it is not a Darlington pair.

It has similar characteristics:

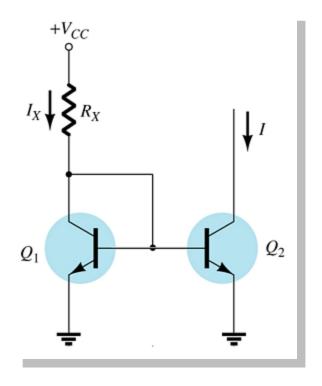
- High current gain
- Voltage gain near unity
- Low output impedance
- High input impedance

The difference is that a Darlington uses a pair of like transistors, whereas the feedback-pair configuration uses complementary transistors.



Current Mirror Circuits

Current mirror circuits provide constant current in integrated circuits.



Example 4.26: Calculate the mirrored current I in the above circuit given, $R_x = 1.1 \text{ k}\Omega$ and $+V_{cc} = 12 \text{ V}$.